App No.: Not Yet Assigned Inventor: Rumi Sheryar Gonda

Docket No.: RGQ-005

Title: SUPPORTING SDH/SONET APS BRIDGE SELECTOR FUNCTIONALITY FOR ETHERNET

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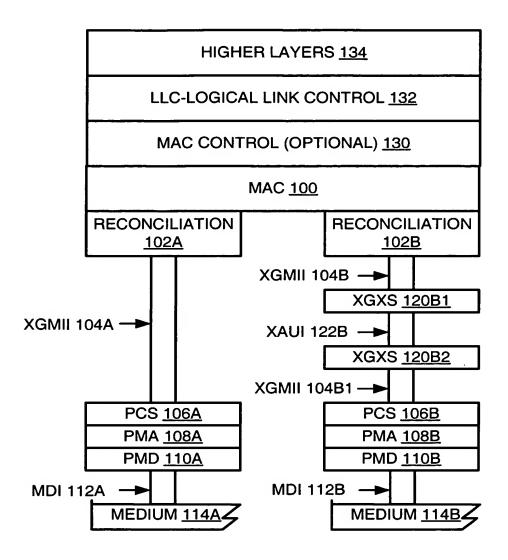


FIG. 1 (Prior Art)

220 XGMII Signals - RXD<31:0> 226 TXD<31:0> 222 ► TXC<3:0> 223 ► TX_CLK 224 200 Reconciliation Sublayer PLS_DATA_VALID.indicate 217 PLS_CARRIER.indicate 218 210 PLS Service Primitives PLS_SIGNAL.indicate 214 PLS_DATA.indicate 216 PLS_DATA.request 212

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FIG. 2 (Prior Art)

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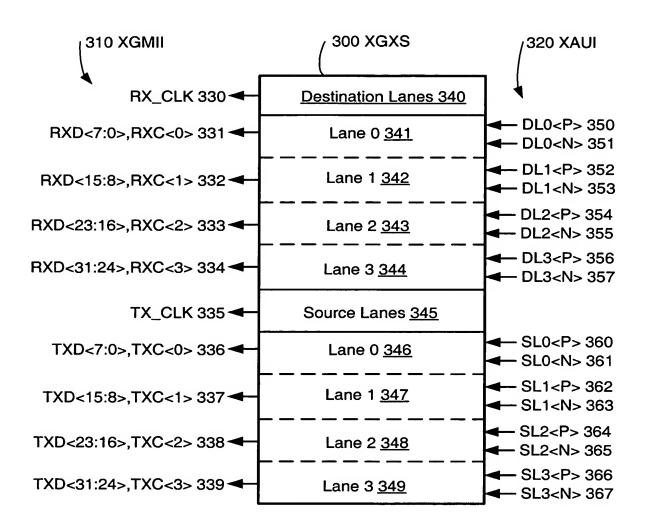
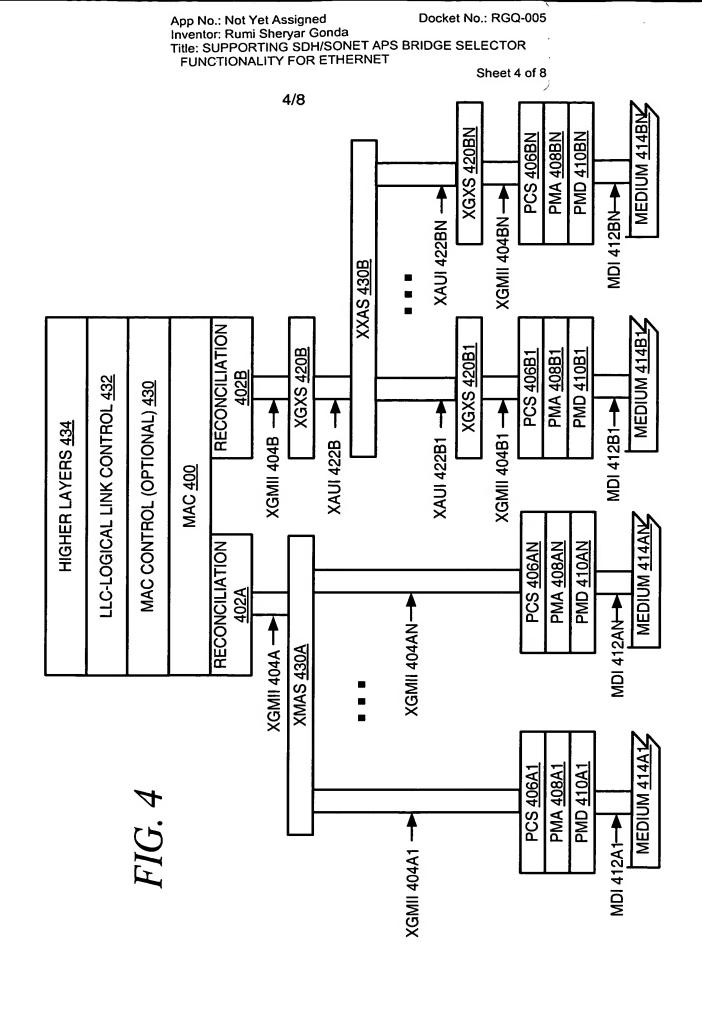


FIG. 3 (Prior Art)



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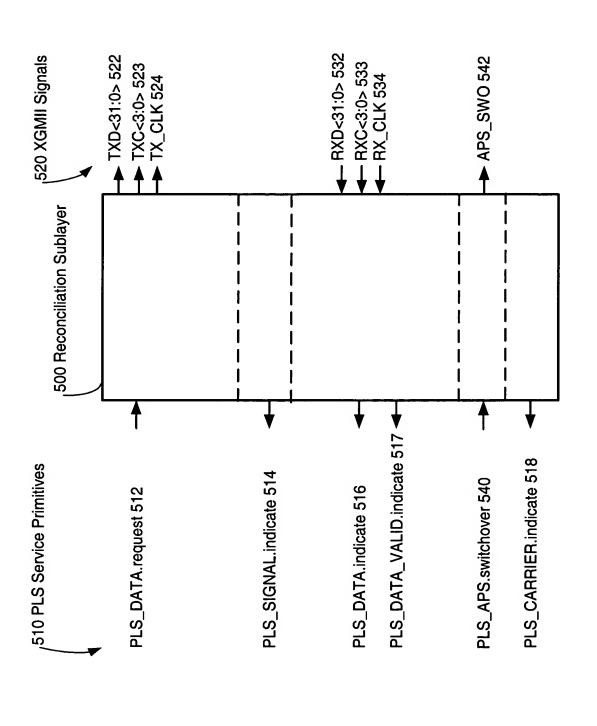


FIG. 5

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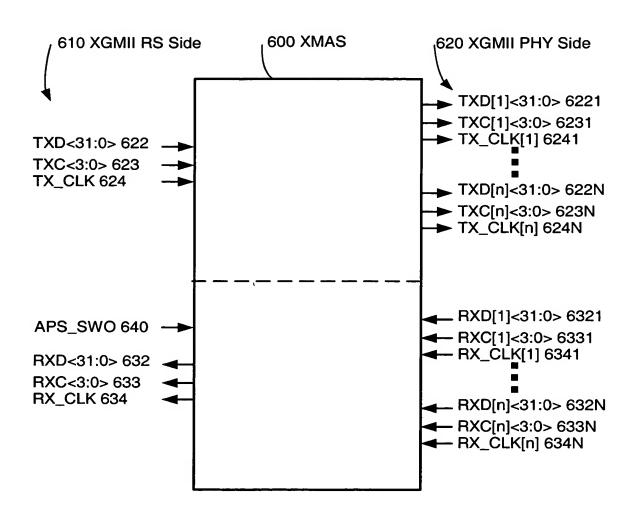


FIG. 6

FUNCTIONALITY FOR ETHERNET Sheet 7 of 8 7/8 710 XAUI RS Side 720 XAUI PHY Side **700 XXAS** APS_SWO 770-**Destination Lanes** 740 - DL0[1]<P> 7501 DL0[1]<N> 7511 DL0<P> 750 ◀ Lane 0 741 DL0<N> 751 ◀ DL0[n]<P> 750N DL0[n]<N> 751N - DL1[1]<P> 7521 DL1[1]<N> 7531 DL1<P> 752 ◀ Lane 1 742 DL1<N> 753 ◀ - DL1[n]<P> 752N DL1[n]<N> 753N DL2[1]<P> 7541 DL2[1]<N> 7551 DL2<P> 754 < Lane 2 743 DL2<N> 755 ◀ DL2[n]<P> 754N - DL2[n]<N> 755N DL3[1]<P> 7561 DL3<P> 756 DL3[1]<N> 7571 Lane 3 744 DL3<N> 757 -DL3[n]<P> 756N DL3[n]<N> 757N Source Lanes 745 ► SL0[1]<P> 7601 · SL0[1]<N> 7611 SL0<P> 760 -Lane 0 746 SL0<N> 761 -SL0[n]<P> 760N ➤ SL0[n]<N> 761N ► SL1[1]<P> 7621 - SL1[1]<N> 7631 SL1<P> 762 -Lane 1 <u>747</u> SL1<N> 763 -SL1[n]<P> 762N ➤ SL1[n]<N> 763N ► SL2[1]<P> 7641 ➤ SL2[1]<N> 7651 SL2<P> 764 · Lane 2 748 SL2<N> 765 -► SL2[n]<P> 764N ➤ SL2[n]<N> 765N ➤ SL3[1]<P> 7661 SL3<P> 766 · - SL3[1]<N> 7671 Lane 3 <u>749</u> SL3<N> 767 · ➤ SL3[n]<P> 766N SL3[n]<N> 767N FIG. 7

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